

A METHOD AND APPARATUS FOR BUILDING UP
LARGE SCALE ON CHIP DE-COUPLING CAPACITOR
ON STANDARD CMOS/SOI TECHNOLOGY

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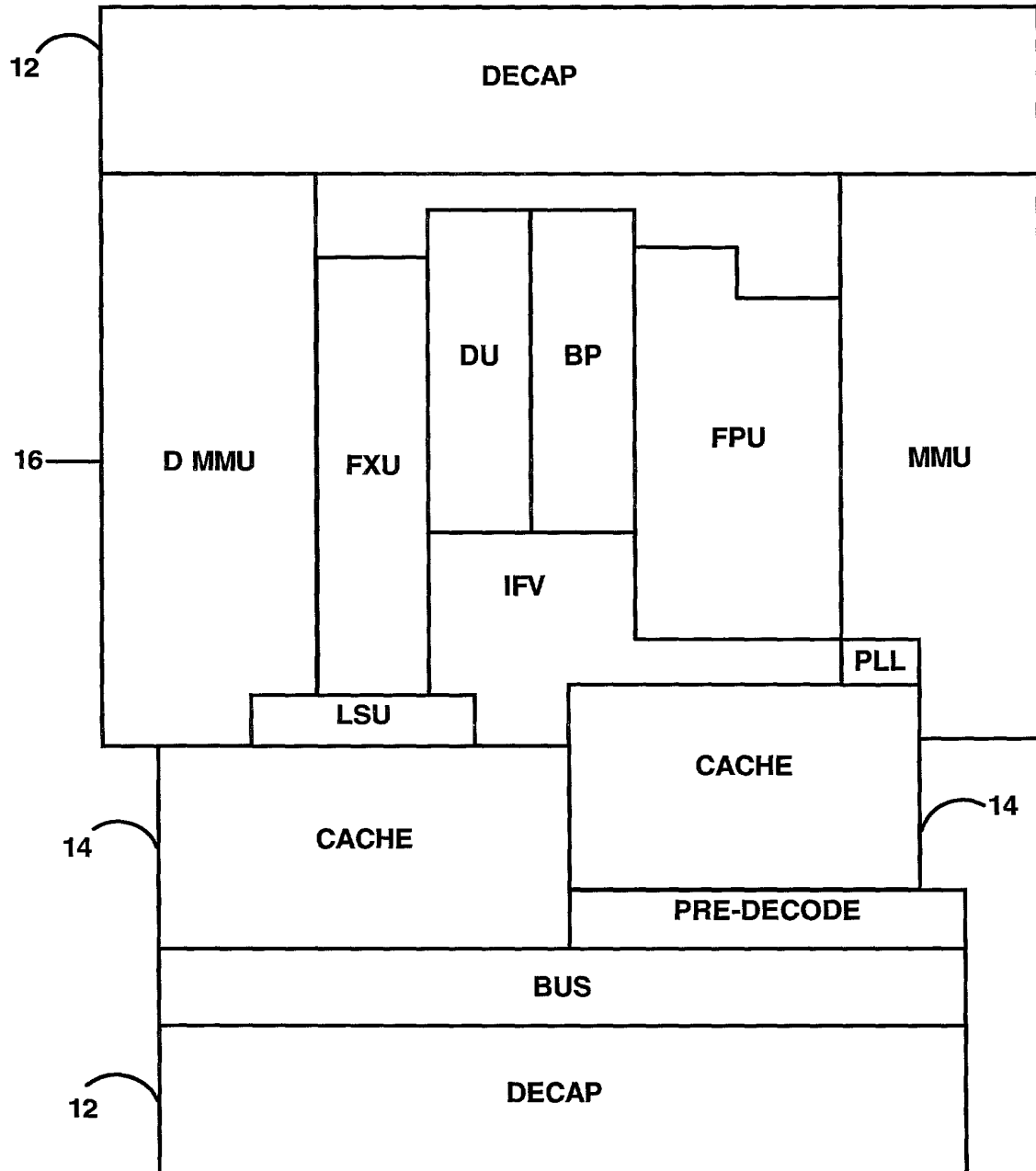


FIG. 1

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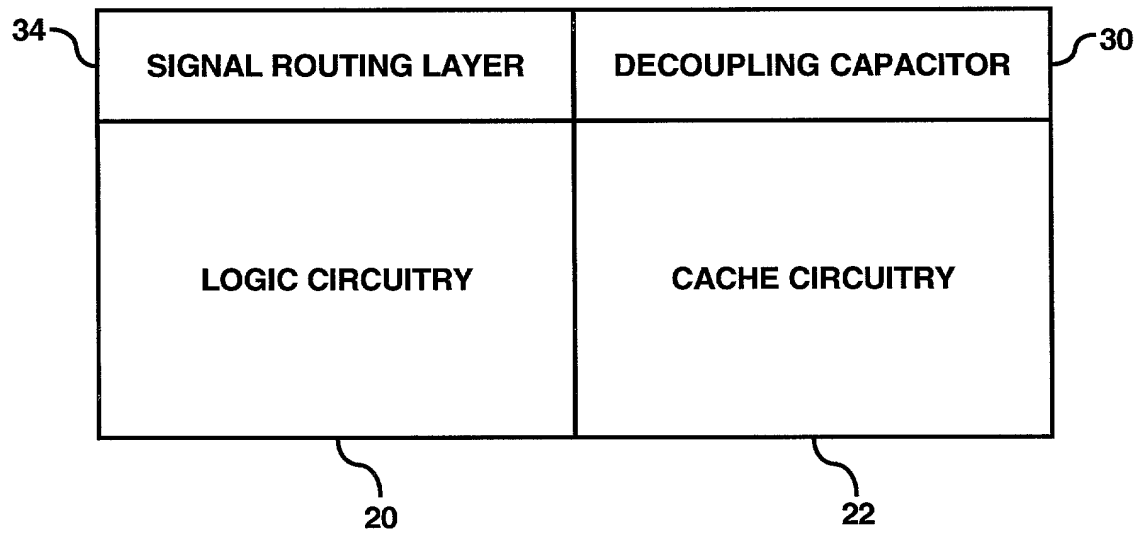


FIG. 2

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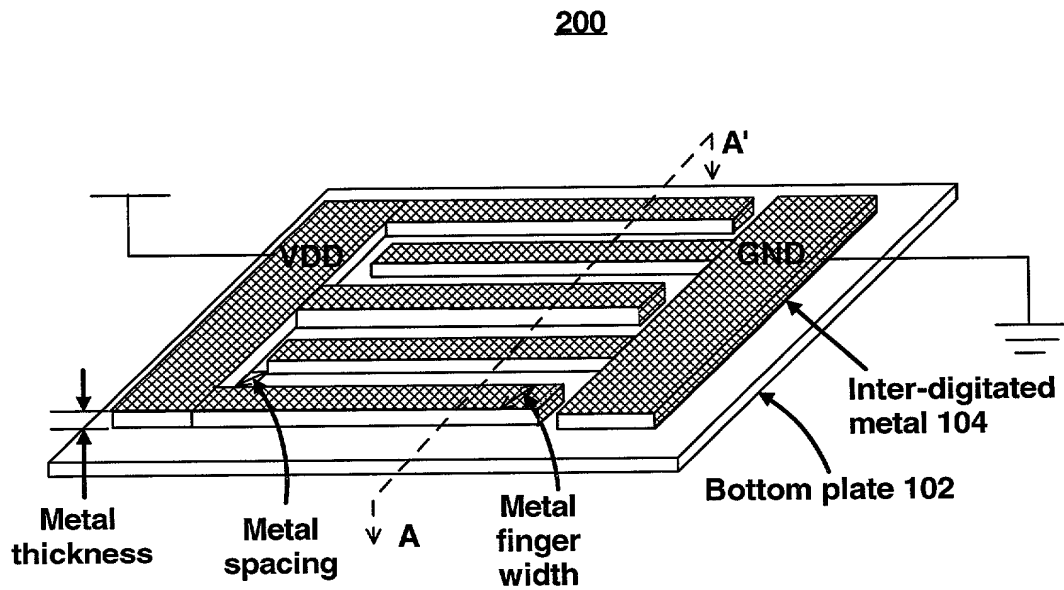


FIG. 3

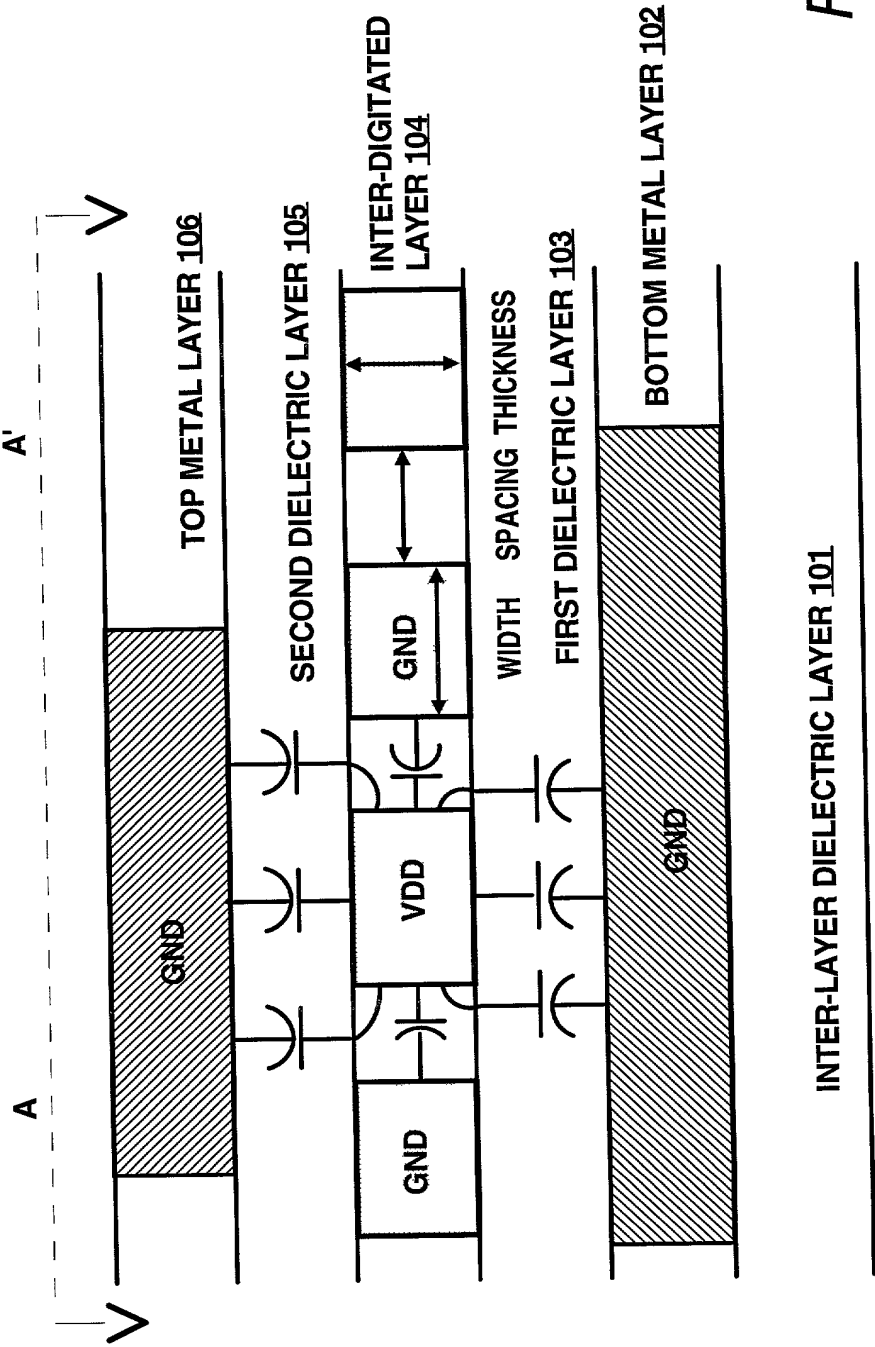


FIG. 4